

# SYSTEMS I — LAB 5

## An introduction to *mini-k* ISA

Revision 1 [A]

In today's lab, we will do some machine code programming. In particular, we're going to use a program that simulates the *mini-k* processor—an artificially created CPU for this course. You will write some assembly code, and then you will convert it to machine code by hand, enter that into a file, and use that file as the contents of RAM on a simulator computer.

## Introduction to assembly/machine-code programming

The following steps will lead you through your first assembly language program. Here, the goal is to get used to the tools involved in writing, assembling, testing, and debugging these programs. After you get this pre-written and simple program working, you will then need to write a program of your own.

1. **Login:** In order to use the simulator, you must first logon to `romulus.amherst.edu` or `remus.amherst.edu`, which are UNIX (Linux) systems. To login using *Xming*, software that allows you to login graphically to these servers, follow the Windows *Xming* instructions that describe how to use this software on the Windows machines in Seeley Mudd 014. Notice that this page also describes how to install and use *Xming* on your Windows machine. If you have a Mac, follow the Mac *Xming* instructions.
2. **Make a directory:** When you first login, you will be working in your *home directory*—the UNIX analog of your *My Documents* folder. Within this directory, you should make a *subdirectory* (a folder) for your work for this lab. Specifically, enter the following command to create and then change into that subdirectory:

```
$ mkdir lab-5
$ cd lab-5
```

3. **Create a machine code file:** Use a special command to create a new file into which you will write your machine code (and later load it into the simulator):

```
$ touch just-halt.img
```

4. **Write the machine code:** Run a special program called `hexedit` in order to add and change byte values, in hexadecimal, to a machine code file. Within this program, you will be able to type the values held in the file, one hexadecimal nybble at a time, while a text version of the file is shown on the right side of the screen. Notice that for most byte-sized values that you create here, little meaningful text will be created in that space. We really only care of about the binary values, and not much about the text:

```
$ hexedit just-halt.img
```

Within this program create a *one-byte machine code program where the bits are all 0*. By typing `00`, you will create that byte with that value. That opcode is for the HALT instruction, which is all that we want this first program to do.

Then type `F2` to save the change to the file, and then *Control-X* to exit `hexedit`, bringing you back to the shell prompt.

5. **Run the simulator:** Start the *mini-k simulator*—a program that reads and runs the machine code produced by the assembler, just like a real CPU would:

```
$ k-simulator just-halt.img
```

```
WARNING: The runtime version supported by this
         application is unavailable.
Using default runtime: v1.1.4322
WARNING: Image file ended at byte 0.
         Zero-filling remaining RAM.
DEBUG:   pc = 0x00  0x00  HALT
Halting!
  acc = 0x00  reg[0] = 0x00  reg[1] = 0x00
         reg[2] = 0x00  reg[3] = 0x00
```

We will examine in class what this output means. The warnings can be ignored; what we care about most begins with each line that begins `DEBUG:`. These lines show you the current *pc* value, the machine code instruction itself, and then a *disassembly* of that instruction: an interpretation of the *opcode* and the *operands*. The instruction is then *executed* (carried out), and the registers—the *accumulator* and the named registers in the *register file*—are shown.

In short, this is the smallest, simplest example of creating and running a program. The machine code file is loaded as the contents of RAM (with extra zeros for those bytes whose values aren't specified, out to 256 bytes of RAM), the *pc* is set to 0, and then the processor proceeds with its *fetch-decode-execute cycle* until it is *halted*.

## Try the program from class

Now that you see how this works, write the program that we've worked out in class. That is, write a program that will:

1. Assign the constant 3 into register `%rA`.
2. Assign the constant 21 into register `%rB`.
3. Add `3 + 21`.

4. Store the final result into register %rC.

Write this program in assembly on paper, and then work out the machine code that follows from it. Use `hexedit` to then create that machine code file and test your program on the simulator.

The following table may be handy:

```
HALT = 0x0,    // Stop processing
COPY = 0x1,    // Copy from the accumulator to a register.
NOT   = 0x2,    // Bitwise inversion
AND   = 0x3,    // Bitwise logical conjunction
OR    = 0x4,    // Bitwise logical disjunction
ADD   = 0x5,    // Arithmetic addition
SUB   = 0x6,    // Arithmetic subtraction
LOAD  = 0x7,    // Copy from main memory to a register
STOR  = 0x8,    // Copy from a register to main memory
SEQ   = 0x9,    // Set the accumulator if Equal
SLT   = 0xa,    // Set the accumulator if Less Than
SGT   = 0xb,    // Set the accumulator if Greater Than
BRIS  = 0xc,    // BRanch If the given register is Set
BRIC  = 0xd,    // BRanch If the given register is Clear
SLA   = 0xe,    // Set the Lower nybble of the Accumulator
SUA   = 0xf     // Set the Upper nybble of the Accumulator
```

## A trickier challenge, using main memory

Place the values 21 and 3 into your image value so that it will be loaded into RAM. Then use the `LOAD` instruction to copy these values into registers, `SUBTRACT` them, and then use the `STOR` instruction to move the result into the highest address in RAM (`0xffff`).

## How to submit your work

We will be using the `cs16-submit` command to turn in programming work. Specifically, you should submit your completed work like so, from within your `lab-5` directory:

```
cs16-submit lab-5 *.img
```

**This assignment is due on Friday, November 5, at 11:00 am**

## A Revision history

Here is a list of the revisions of this document, including links to each, and the dates at which they were published:

- **Revision 0 [2010-Oct-29]:** The initial, complete revision.
- **Revision 1 [2010-Nov-07]:** Previous postings of Revision 0 were inconsistent on providing the use of the `touch` command; this version includes that command to create the image file before invoking `hexedit`. Additionally, the description of the opcodes `BRIS` and `BRIC` were changed to reflect an update to the ISA in which the register tested is a named register instead of the accumulator.